



Enhancement of Power Quality by Using Shunt Hybrid Power Filter With TCR

D. Srikanth¹, V. Sreepriya²

¹M. Tech scholar, Department of EEE, S.R Engineering College, Warangal, T.S, India

²Assistant Professor, Department of EEE, S.R Engineering College, Warangal, T.S, India

Abstract:- This paper presents a thyristor-controlled reactor (TCR) in combination with a fifth-tuned LC passive filter and active power filter (APF) in order to compensate the reactive power and to reduce harmonics. In addition, it reduces significantly the volt-ampere rating of the APF part. The shunt hybrid power filter (SHPF) is the combined system of a small-rating APF connected in series with a fifth-tuned LC passive filter. A control scheme based on proportional-integral controller has been used to mitigate the harmonics and reactive power. The proposed system also helps in achieving a low cost highly effective control.

Keywords- Power Quality, harmonics, shunt hybrid power filter, thyristor-controlled reactor, reactive power.

I. INTRODUCTION

Now a day's power electronic equipment's are mostly used in industrial and domestic purpose. This equipment's have major impact on the quality of supply voltage and have increased the harmonic current content of distribution systems. They have many negative effects on distribution system side equipment's and customer, such as additional losses in overhead and underground cables, transformers and rotating electric machines, problem in the operation of the protection systems, over voltage and shunt capacitor, error of measuring instruments, and malfunction of low efficiency of customer sensitive loads. The most important power quality problems are harmonics and high neutral current. The definition of power quality given in the IEEE dictionary originates in IEEE Std1100: Power quality is the concept of powering and grounding sensitive equipment in a manner that is suitable to the operation of that equipment. Power quality problems are common in most of commercial and industrial networks. Natural phenomena, such as lightning are the most frequent cause of power quality problems. Also, the connection of high power non-linear loads contributes to the generation of current and voltage harmonic content the major and critical power quality problems are voltage sags due to the high economical losses that can be generated [1]. For all these reasons, from the consumer point of view, power quality issues will become an increasingly important

factor to consider in order satisfying good productivity. On the other hand, for the electrical supply industry, the quality of power delivered will be one of the distinguishing factors for ensuring customer loyalty in this very competitive and deregulated market. Reactive power, Power factor, Harmonic distortion and Voltage unbalance in power supply are the major problems of power quality. According to IEEE-519, total harmonics distortion is defined as the summation of the effective value of the harmonics components in the distorted waveform relative to the fundamental component.

Passive filter have been used traditionally for minimizing the distortion due to harmonic current in industrial power systems. But they have many drawbacks such as resonance problem, dependency of their performance on the system impedance, absorption of harmonic current of nonlinear load, which could lead to further harmonic propagation through the power system [2]. To overcome those problems active power filters is introduced. It has no such drawbacks like passive filter. They inject harmonic voltage or current with appropriate magnitudes and phase angle into the system and cancel harmonics of nonlinear loads [9]. But it has also some drawbacks like high initial cost and high power losses due to which it limits there wide application, especially with high power rating system [3].

To minimize these limitations, hybrid power filter have been presented and implemented in practical system applications[4] - [8]. Shunt hybrid filter consists of an active power filter which is connected in series with the passive filter and with a three phase PWM inverter. This filter effectively mitigates the problem of a passive and active filter when used alone. It provides cost effective harmonic compensation, particularly for high power nonlinear load [5]. Several filter topologies for compensating harmonics and reactive power have been proposed in the literature [9]–[12]. In [9], a multi-converter conditioner topology formed by an active conditioner operating in parallel with a hybrid conditioner has been proposed. The hybrid conditioner is a combination of one or more passive filters in series with a low-rated active power filter (APF). The conditioner compensates harmonic distortion, imbalance, and reactive power in three-phase four-wire systems. This topology constitutes an effective solution

at high-powerlevels, which is cost-effective because of the kilovoltampererating reduction of the inverters. A hybrid consists of a three-phase three-level neutral pointclamped (NPC) inverter and a series connection of a three-levelH-bridge inverter with a novel control scheme to control thefloating voltage source of the H-bridge stage has been introduced in [10]. In this topology, the NPC inverter is used to supplythe total active power while the H-bridges operate as seriesactive filters for the harmoniccompensation of the NPC outputvoltage. The rating of the series active filter is reduced becauseit provides only the reactive power for the operationof the floating capacitor.Different control techniques are present for extracting harmonic components of the source current. Some of them are synchronous reference frame (SRF) transformation, instantaneous power (p-q) theory, etc. where high pass filters (HPF) are used or extracting harmonic components of the source current from the fundamental components [6].

The proposed combination of SHPF and TCR is as shown Fig. 1 which compensates the reactive power and reduces harmonic currents. In addition, it reduces significantly the volt-ampere rating of the APF part. The SHPF consists of a small-rating APF connected in series with a fifth-tuned LCpassive filter. The APF consists of a three-phase full-bridge voltage-source pulse width modulation (PWM) inverter with an inductor (L_{pf}, R_{pf}) and a dc bus capacitor (C_{dc}). The APF sustains very low fundamental voltages and currents of the power grid, and thus, its rated capacity is greatly reduced.Considering the above mentioned merits, the presented combined topology is very appropriate in compensating reactive power and eliminating harmonic currents in power system. The tuned passive filter in parallel with TCR forms a shunt passive filter (SPF). This latter is mainly for fifth harmonic compensation and power factor correction. The small-rating APF is used to mitigate the harmonics generated by the load and the TCR by enhancing the compensation characteristics of the SPF aside from eliminating the risk of resonance between the grid and the SPF. The TCR goal is to obtain a regulation of reactive power.The simulation results are listed in comparison of different control strategies and for the verification of results.

II. MODELING AND CONTROL OF SHPF

A. Modeling of SHPF

The system equations are first elaborated in 123 reference frame. Using Kirchhoff's voltage law, one can write

$$\begin{aligned} V_{s1} &= L_{pf} \frac{di_{c1}}{dt} + R_{pf} i_{c1} + V_{cpf1} + V_{1m} + V_{mn} V_{s2} \\ &= L_{pf} \frac{di_{c2}}{dt} + R_{pf} i_{c2} + V_{cpf2} + V_{2m} + V_{mn} \\ V_{s3} &= L_{pf} \frac{di_{c3}}{dt} + R_{pf} i_{c3} + V_{cpf3} + V_{3m} + V_{mn} V_{cpf1} \\ &= L_T \frac{di_{c1}}{dt} - C_{pf} L_T \frac{d^2 V_{cpf1}}{dt^2} V_{cpf2} \\ &= L_T \frac{di_{c2}}{dt} - C_{pf} L_T \frac{d^2 V_{cpf2}}{dt^2} V_{cpf3} \\ &= L_T \frac{di_{c3}}{dt} - C_{pf} L_T \frac{d^2 V_{cpf3}}{dt^2} \frac{dV_{dc}}{dt} \\ &= \frac{1}{C_{dc}} i_{dc} \end{aligned} \quad (1)$$

The switching function C_k of the k_{th} leg of the converter (for $k = 1, 2, 3$) is defined as

$$C_k = \begin{cases} 1 & \text{if } s_k \text{ is on and } s'_k \text{ is off} \\ 0 & \text{if } s_k \text{ is off and } s'_k \text{ is on} \end{cases} \quad (2)$$

A switching state function d_{nk} is defined as

$$d_{nk} = \{c_k - \frac{1}{3} \sum_{m=1}^3 (c_m)\}_n \quad (3)$$

Moreover, the absence of the zero sequence in the ac currents and voltages and in the $[d_{nk}]$ functions leads to the following transformed model in the three-phase coordinates

$$\begin{aligned} L_{pf} \frac{di_{c1}}{dt} &= -R_{pf} i_{c1} - d_{n1} V_{dc} - V_{cpf1} + v_{s1} \\ L_{pf} \frac{di_{c2}}{dt} &= -R_{pf} i_{c2} - d_{n2} V_{dc} - V_{cpf2} + v_{s2} \\ L_{pf} \frac{di_{c3}}{dt} &= -R_{pf} i_{c3} - d_{n3} V_{dc} - V_{cpf3} + v_{s3} \\ C_{dc} \frac{dV_{dc}}{dt} + \frac{V_{dc}}{R_{dc}} &= d_{n1} i_{c1} + d_{n2} i_{c2} + d_{n3} i_{c3} \end{aligned} \quad (4)$$

However, the model is time invariant during a given switching state. Furthermore, the principle of operation of the SHPF requires that the three state variables have to be controlled independently. The interaction between the inner current loop and the outer dc bus voltage loop can be avoided by adequately separating their respective dynamics. Since the steady state fundamental components are sinusoidal, the system is transformed into the d-q reference frame at constant supply frequency. The conversion matrix is

$$C_{dq}^{123} = \sqrt{\frac{2}{3}} \begin{bmatrix} \cos \theta & \cos \left(\theta - \frac{2\pi}{3} \right) & \cos \left(\theta - \frac{4\pi}{3} \right) \\ -\sin \theta & -\sin \left(\theta - \frac{2\pi}{3} \right) & -\sin \left(\theta - \frac{4\pi}{3} \right) \end{bmatrix} \quad (5)$$

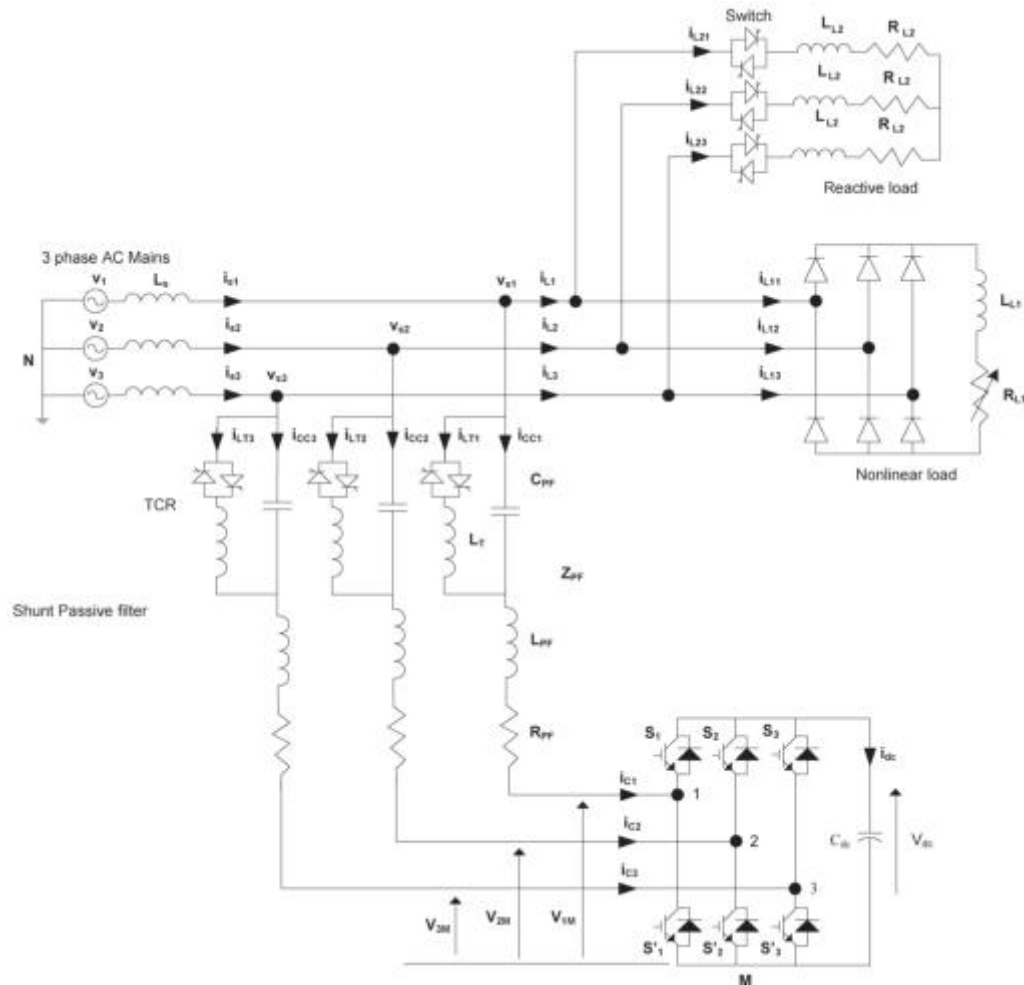


Fig. 1 Basic Circuit of SHPF-TCR Compensator

Where $\mathbf{C}^{dq}_{123} = \mathbf{t}$ and the following equalities hold: $\mathbf{C}^{dq}_{123} = (\mathbf{C}^{123}_{dq})^{-1} = (\mathbf{C}^{123}_{dq})^T$.

Then, by applying dqtransformation, the state space model of the system in the synchronous reference frame is obtained.

B. Controlling of Harmonic Current

A nonlinear control of SHPF is developed for current tracking and voltage regulation purposes. It is based on a decoupled control strategy, which considers that the controlled system may be divided into an inner fast loop and an outer slow dc voltage loop, is adopted. Note that the first and the second time derivative TCR capacitor voltages have no significant negative impact on the performance of the proposed control technique because their coefficients are too low. Consequently, they can practically be ignored.

By using the d-q transformation, the decoupled dynamics of the current tracking is obtained. The currents i_d and i_q can be controlled independently. Furthermore, by using proportional integral compensation, a fast dynamic response and zero steady-

state errors can be achieved. The expressions of the tracking controllers are

$$\begin{aligned} u &= (L_{pf}(1 - C_{pf}L_T\omega^2) + L_T)\frac{di_d}{dt} \\ &\quad + R_{pf}(1 - C_{pf}L_T\omega^2)i_d \\ &= k_p\bar{i}_d + k_i\int \bar{i}_d dt \\ u_q &= (L_{pf}(1 - C_{pf}L_T\omega^2) + L_T)\frac{di_q}{dt} \\ &\quad + R_{pf}(1 - C_{pf}L_T\omega^2)i_q \\ &= k_p\bar{i}_q + k_i\int \bar{i}_q dt \end{aligned} \quad (6)$$

Where $\bar{i} = i^*_d - i_d$ and $\bar{i}_q = i^*_q - i_q$ are current errors and i^*_d and i^*_q denote the reference signals of i_d and i_q respectively.

The transfer function of the proportional-integral controllers is given as

$$\begin{aligned} G_{i1}(s) &= \frac{U(s)}{\bar{i}(s)} = k_{p1} + \frac{k_{i1}}{s} \\ G_{i2}(s) &= \frac{U_q(s)}{\bar{i}_q(s)} = k_{p2} + \frac{k_{i2}}{s} \end{aligned} \quad (7)$$

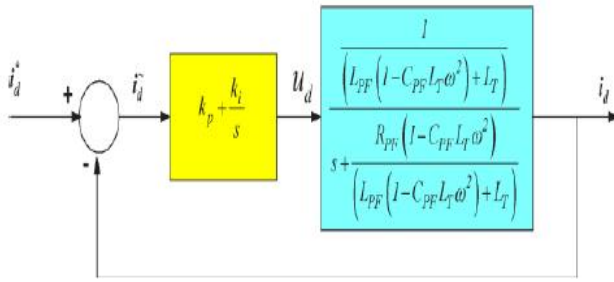


Fig. 2 Inner Control Loop of the Current i_d

The inner control loop of the current i_d is shown in Fig. 2. The closed-loop transfer functions of the current loops are

$$\frac{I(s)}{I^*(s)} = \frac{k_{p1}}{A} \frac{(s + \frac{k_{i1}}{k_{p1}})}{s^2 + (\frac{B+k_{p1}}{A})s + k_{i1}}$$

$$\frac{I(s)}{I^*(s)} = \frac{k_{p2}}{A} \frac{(s + \frac{k_{i2}}{k_{p2}})}{s^2 + (\frac{B+k_{p2}}{A})s + k_{i2}} \quad (8)$$

Where $A = L_{pf}(1 - C_{pf}L_T \omega^2) + L_T$ and $B = R_{pf}(1 - C_{pf}L_T \omega^2)$.

The closed-loop transfer functions of the current loops have the following form

$$\frac{I(s)}{I^*(s)} = 2 \frac{\omega_{ni}}{s^2 + 2\zeta\omega_{ni}s + \omega_{ni}^2} \quad (9)$$

where ω_{ni} is the outer loop natural angular frequency and ζ is the damping factor. For the optimal value of the damping factor $\zeta = \sqrt{2}/2$, the theoretical overshoot is 20.79%. The following design relations can be derived

$$k_{p1} = k_{p2} = 2\zeta\omega_{ni}(L_{pf}(1 - C_{pf}L_T\omega^2) + L_T) - R_{pf}(1 - C_{pf}L_T\omega^2)$$

$$k_{i1} = k_{i2} = (L_{pf}(1 - C_{pf}L_T\omega^2) + L_T)\omega_{ni}^2 \quad (10)$$

Note that the inputs q_{nd} and q_{nq} consist of a nonlinearity cancellation part and a linear decoupling compensation part.

C. DC Bus Voltage Regulation

The active filter produces a fundamental voltage which is in-phase with fundamental leading current of the passive filter. A small amount of active power is formed due to the leading current and fundamental voltage of the passive filter and it delivers to the dc capacitor. Therefore, the electrical quantity adjusted by the dc-voltage controller is consequently. The outer control loop of dc bus voltage is as shown in Fig. 3. To maintain equal to its reference value, the losses through the active filters resistive-inductive branches will be compensated by acting on the supply current equation(4) can be rewritten as

$$C_{dc} \frac{dV_{dc}}{dt} + \frac{V_{dc}}{R_{dc}} = d_{nq} i_q \quad (11)$$

The three-phase filter currents are given by

$$\begin{bmatrix} i_{c1} \\ i_{c2} \\ i_{c3} \end{bmatrix} = \sqrt{\frac{2}{3}} i_q \begin{bmatrix} -\sin \theta \\ -\sin(\theta - \frac{2\pi}{3}) \\ -\sin(\theta - \frac{4\pi}{3}) \end{bmatrix} \quad (12)$$

The fundamental filter r.m.s current I_c is

$$I_c = \frac{i_q}{\sqrt{3}} \quad (13)$$

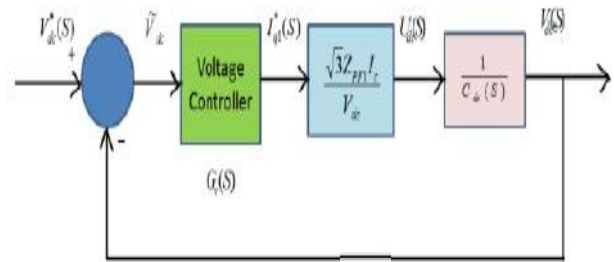


Fig. 3 outer control loop of the dc bus voltage

The q-axis active filter voltage V_{Mq} is expressed as

$$V_{Mq} = q_{nq} V_{dc} = -Z_{pf1} i_{q1}^* \quad (14)$$

Where Z_{pf1} is the impedance of the passive filter at 60 Hz and i_{q1}^* is a dc component.

$$u_{dc} = q_{nq} i_q \quad (15)$$

The control effort of the dc voltage loop is deduced

$$i_{q1}^* = \frac{V_{dc}}{-Z_{pf1} i_q} u_{dc} \quad (16)$$

The dc component will force the SHPF-TCR compensator to generate or to draw a current at the fundamental frequency.

To regulate the dc voltage V_{dc} the error $\bar{V}_{dc} = V_{dc}^* - V_{dc}$ is passed through a PI-type controller given by

$$u_{dc} = k_1 \bar{V}_{dc} + k_2 \int \bar{V}_{dc} dt \quad (17)$$

The response of the dc bus voltage loop is a second-order transfer function and has the following form:

$$\frac{V_{dc}(s)}{V_{dc}^*(s)} = 2 \frac{\omega_{nv}}{s^2 + 2\zeta\omega_{nv}s + \omega_{nv}^2} \quad (18)$$

The closed-loop transfer function of dc bus voltage regulation is given as follows:

$$\frac{V_{dc}(s)}{V_{dc}^*(s)} = \frac{\frac{\sqrt{3}Z_{pf1}k_p I_c}{\bar{V}_{dc}C_{dc}}s + \frac{\sqrt{3}Z_{pf1}k_i I_c}{\bar{V}_{dc}C_{dc}}}{s^2 + \frac{\sqrt{3}Z_{pf1}k_p I_c}{\bar{V}_{dc}C_{dc}}s + \frac{\sqrt{3}Z_{pf1}k_i I_c}{\bar{V}_{dc}C_{dc}}} \quad (19)$$

Where \bar{V}_{dc} is the average value of dc voltage which is equal to 50volts. The proportional k_1 and k_2 integral gains are then obtained as follows:

$$k_1 = 2 \omega_{nv} (\bar{V}_{dc} C_{dc} / 3Z_{PF1} I_c),$$

$$k_2 = \frac{2}{\omega_{nv}} (\bar{V}_{dc} C_{dc} / 3Z_{PF1} I_c).$$

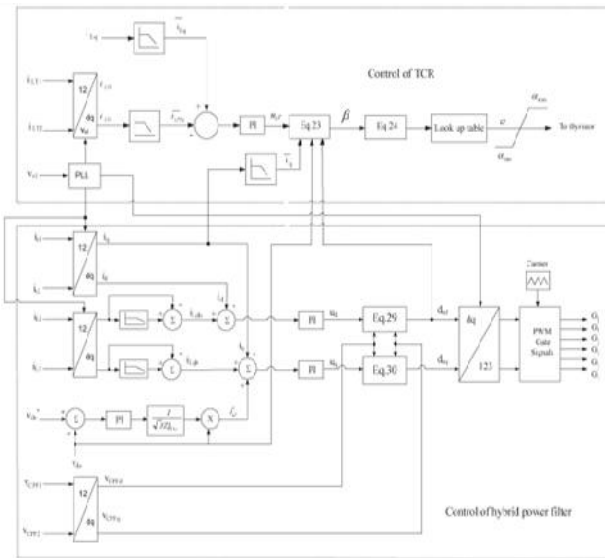


Fig. 4 Schematic diagram of SHPF and TCR controller

By designing the dc bus voltage loop much slower than the current one, there would not be any interaction between the two loops. The controller of the proposed SHPF and TCR system is as shown in Fig. 4. The system parameters are given in Table I.

TABLE I: SYSTEM PARAMETERS

Line to Line source voltage and frequency	$V_{s-L-L} = 208 \text{ V}, f_s = 60 \text{ Hz}$
Line impedance	$L_{s-L-L} = 0.5 \text{ mH}, R_{s-L-L} = 0.1$
Nonlinear load	$L_{n-L-L} = 10 \text{ mH}, R_{n-L-L} = 27$
Linear load	$L_{l-L-L} = 20 \text{ mH}, R_{l-L-L} = 27$
Passive filter parameters	$L_{pf-L-L} = 2 \text{ mH}, C_{pf-L-L} = 40 \mu\text{F}$
Active filter parameters	$C_{af-L-L} = 3000 \mu\text{F}, R_{af-L-L} = 1 \text{ k}$
DC bus voltage of APF of SHPF	$V_{dc} = 50 \text{ V}$
Switching frequency	1920 Hz
Inner controller parameters	$K_{p1} = K_{p2} = 43.38$ $K_{i1} = K_{i2} = 37408$
Outer controller parameter	$K_{f1} = 0.26, K_{f2} = 42$
Cut off frequency of the low pass filters	$F_c = 70 \text{ Hz}$
TCR inductance	$L_T = 25 \text{ mH}$

III. MODELING AND EXTRACTION OF FIRING ANGLE OF TCR

TCR equivalent circuit is as shown in Fig. 5. Using Kirchhoff's voltage law, the following equations in 123 reference frame are obtained

$$\begin{aligned} V_{s1} &= L_{pf} \frac{di_{c1}}{dt} + L_T \frac{dL_{T1}}{dt} + R_{pf} i_{c1} + d_{n1} V_{dc} \\ V_{s2} &= L_{pf} \frac{di_{c2}}{dt} + L_T \frac{dL_{T2}}{dt} + R_{pf} i_{c2} + d_{n2} V_{dc} \\ V_{s3} &= L_{pf} \frac{di_{c3}}{dt} + L_T \frac{dL_{T3}}{dt} + R_{pf} i_{c3} + d_{n3} V_{dc} \end{aligned} \quad (20)$$

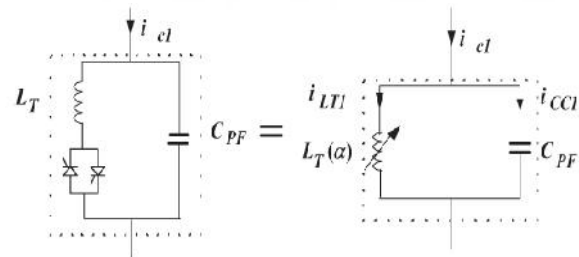


Fig. 5 TCR equivalent circuit

Applying Park's transformation to above equation we get

$$L_T \left(\frac{di_{LTd}}{dt} \right) = L_T \left(\omega_{i_{LTq}} + L_{pf} \omega_{i_q} - L_{pf} \frac{di_d}{dt} - R_{pf} i_d - d_{nd} V_{dc} + V \right)$$

$$L_T(\alpha) \frac{di_{LTq}}{dt} = -L_T(\alpha) i_{LTd} - L_{pf} \omega_{i_d} - L_{pf} \frac{di_q}{dt} - R_{pf} i_q - d_{nq} V_{dc} + V_q \quad (21)$$

The reactive part is chosen to control the reactive current so that $V_q = 0$ and $L_T(\alpha) i_{LTd} = 0$

$$\frac{di_{LTq}}{dt} = B(\alpha) \left[-L_{pf} \omega_{i_d} - L_{pf} \frac{di_q}{dt} - R_{pf} i_q - d_{nq} V_{dc} \right] \quad (22)$$

Where $B(\alpha) = 1/L_{pf}(\alpha)\omega$ is the susceptance.

An equivalent input u_{qT} is defined as

$$u_{qT} = \frac{di_{LTq}}{dt} \quad (23)$$

According to this expression, one deduces

$$B(\alpha) = \frac{u_{qT}}{\left[-L_{pf} \omega_{i_d} - L_{pf} \frac{di_q}{dt} - R_{pf} i_q - d_{nq} V_{dc} \right]} \quad (24)$$

On the other hand, the equivalent inductance is given by

$$L_{pf}(\alpha) = L_{pf} \frac{2\pi - 2 + \sin(2\alpha)}{2\pi - 2 + \sin(2\alpha)} \quad (25)$$

The susceptance is given by

$$B(\alpha) = B \frac{2\pi - 2 + \sin(2\alpha)}{2\pi - 2 + \sin(2\alpha)} \quad (26)$$

Where $B = 1/L_{PF0}$.

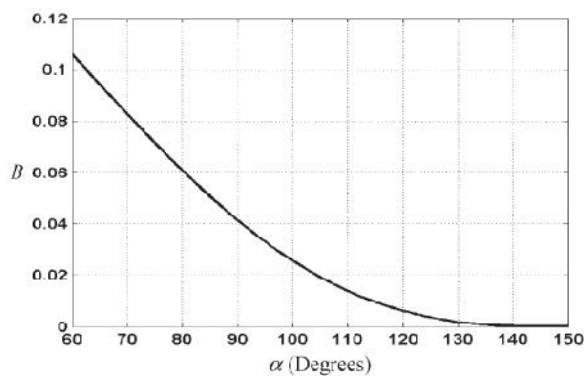


Fig.6. Susceptance versus firing angle.

Fig.6. illustrates the susceptance versus firing angle. A PI controller is used to force the reactive current of the SHPF-TCR compensator to follow exactly the reactive current consumed by the load.

IV. SIMULATION MODEL AND RESULTS

Simulations were performed numerically using the “Power System Block set” simulator operating under MATLAB/Simulink environment, in order to verify the operation of the proposed SHPF-TCR compensator using the nonlinear control scheme.

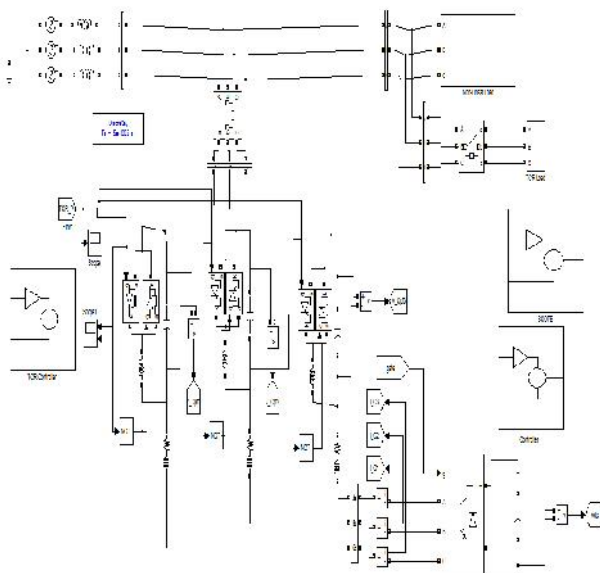


Fig.7 Simulink model of proposed SHPF and TCR combination for power quality enhancement

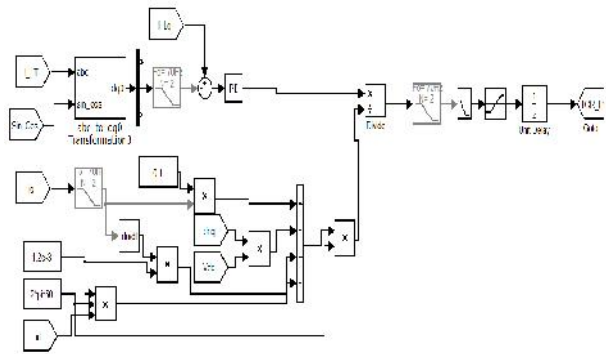


Fig.8 TCR controller

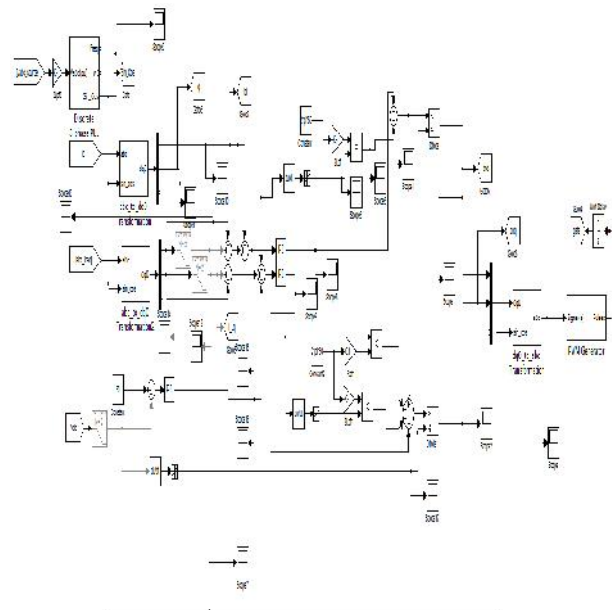


Fig.9 Hybrid power filter controller

Fig.7, Fig.8, Fig.9, are the Simulink model of SHPF – TCR compensator, TCR controller and controller of hybrid power filter respectively. Here simulation is carried out in several cases, in that 1). Proposed Hybrid Power Filter with Steady State Harmonic Generated Load. 2) Transient State Harmonic Load and 3) Transient State Harmonic and Reactive Power Generated Load.

1) Steady State Response of the System for Harmonic Generated Load

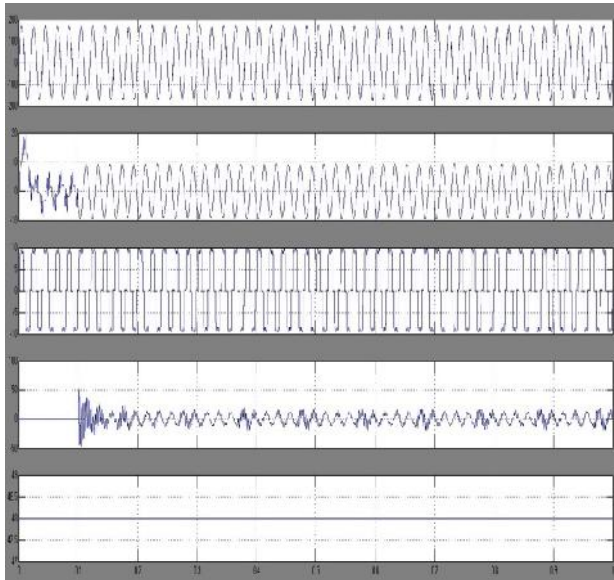


Fig.10 Source Voltage, Source Current, Load Current, Compensation Current, DC Link Voltage.

Fig.10 shows the Steady State Response of Source Voltage, Source Current, Load Current, Compensation Current and DC Link Voltage of the SHPF-TCR Compensator on Harmonic Generation Load.

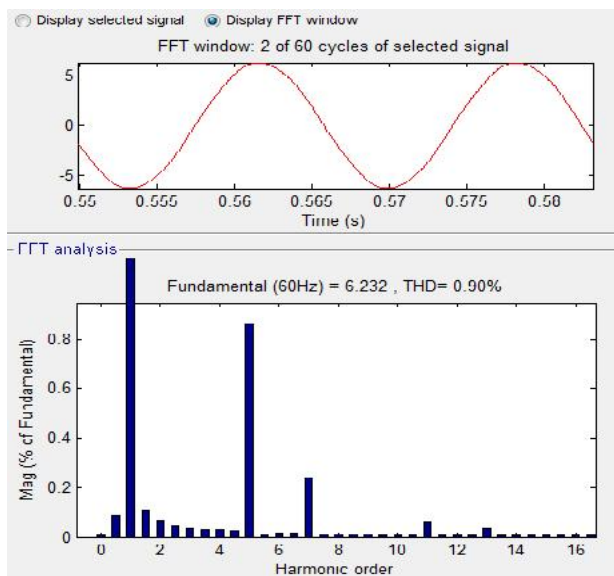


Fig.11 FFT Analysis of Source Current

Fig.11 shows FFT Analysis of Source Current of the SHPF-TCR Compensator on Harmonic Generation Load, attains THD as 0.90%.

2) Transient State Response of the System for Harmonic Generated Load

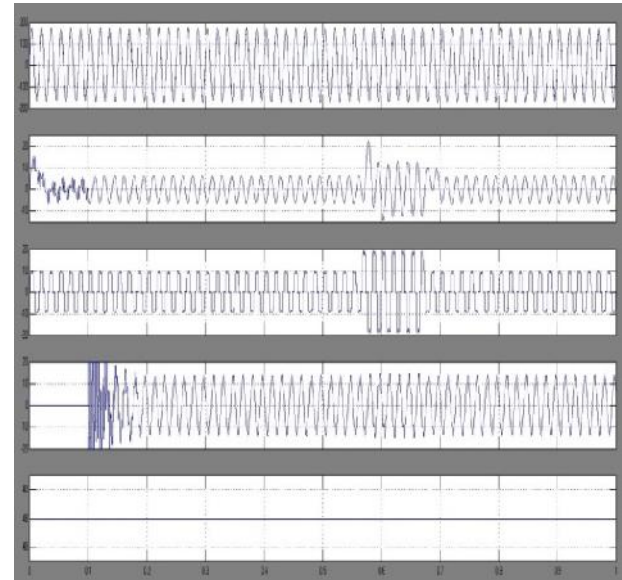


Fig.12 Source Voltage, Source Current, Load Current, Compensation Current, DC Link Voltage

Fig.12 shows the Transient Response of Source Voltage, Source Current, Load Current, Compensation Current and DC Link Voltage of the SHPF-TCR Compensator on Harmonic Generation Load.

3) Transient State Response of the System for Harmonic and Reactive Power Generated Load

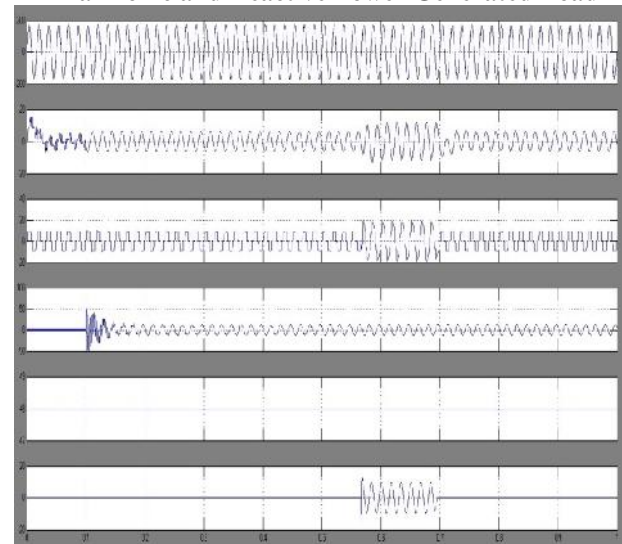


Fig.13 Source Voltage, Source Current, Harmonic Load Current, Compensation Current, DC Link Voltage and Reactive Load Current

Fig.13 shows the Source Voltage, Source Current, Harmonic Load Current, Compensation Current, DC Link Voltage and Reactive Load Current of the Dynamic response of SHPF-TCR compensator under the harmonic and reactive power type of loads. Fig.14 FFT Analysis of Source Current of the SHPF-TCR Compensator on Harmonic and Reactive Power Generation Load attains THD as 0.88%.

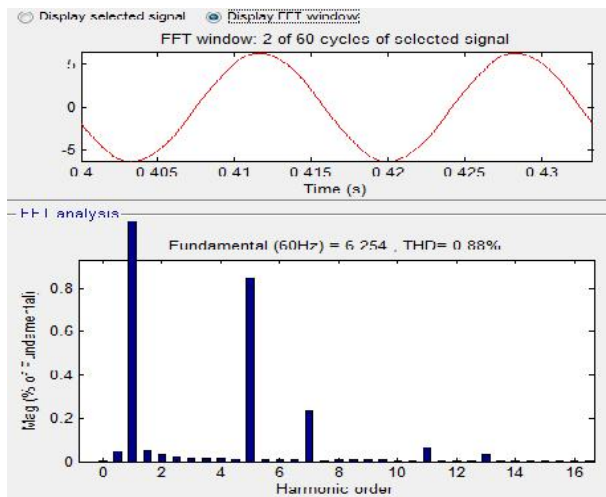


Fig.14 FFT Analysis of Source Current

V. CONCLUSION

In this paper a SHPF – TCR compensator is proposed in order to suppress the current harmonics and reactive power compensation generated by the load. In addition, it reduces significantly the volt-ampere rating of the APF part. The control circuit for the SHPF and the TCR are also explained. A control technique is proposed to improve the dynamic response and decrease the steady-state error of the TCR. The output of the system for various conditions had been explained with the help of the simulation results. The performances of the SHPF – TCR compensator for steady state and transient state of various harmonic and reactive loads are found to be effectively eliminated the current harmonics and reactive power compensation. It has been shown that the system has a fast dynamic response, has good performance in both steady-state and transient operations, and is able to reduce the THD of supply currents.

REFERENCES

- [1] E. F. Fuchs and M. A. S. Masoum, "Power Quality in Electrical Machines and Power Systems," Academic Press, USA, 2008.
- [2] J. C. Das, "Passive filters; potentialities and limitations," IEEE Trans. on Industry Applications, Vol. 40, pp. 232-241, 2004.
- [3] B. Singh, K. Al-Haddad, and A. Chandra, "A review of active filters for power quality improvement," IEEE Trans. Ind. Electron., vol. 46, no. 5, pp. 960-971, Oct. 1999.
- [4] L. Asiminoaei, E. Aeloiza, P. N. Enjeti, and F. B. Laabjerg, "Shunt active-power-filter topology based on parallel interleaved inverters," IEEE Trans. Ind. Electron. vol. 55, no.3, pp. 1175-1189, Mar. 2008.
- [5] H. Fujita, H. Akagi, "A practical approach to harmonic compensation in power systems; series connection of passive and active filters," IEEE Trans. on Industry Applications, Vol. 27, pp. 1020-1025, 1991.

- [6] L. Asiminoaei, E. Aeloiza, P. N. Enjeti, and F. B. Laabjerg, "Shunt active-power-filter topology based on parallel interleaved inverters," IEEE Trans. Ind. Electron. vol. 55, no.3, pp. 1175-1189, Mar. 2008.
- [7] B. Singh, V. Verma, A. Chandra, K. Al-Haddad, "Hybrid filters for power quality improvement," IEEE Proc. on Generation, Transmission and Distribution, Vol. 152, pp. 365-378, 2005.
- [8] Salem Rahmani, Abdelhamid Hamadi, Nassar Mendalek, and Kamal Al-Haddad, "A New Control Technique for Three-Phase Shunt Hybrid Power Filter," IEEE Transactions on industrial electronics, vol. 56, no. 8, pp. 606-805, August 2009.
- [9] M. I. Milanés-Montero, E. Romero-Cadaval, and F. Barrero-González, "Hybrid multiconverter conditioner topology for high-power applications," IEEE Trans. Ind. Electron., vol. 58, no. 6, pp. 2283-2292, Jun. 2011.
- [10] C. A. Silva, L. A. Cordova, P. Lezana, and L. Empringham, "Implementation and control of a hybrid multilevel converter with floating dc links for current waveform improvement," IEEE Trans. Ind. Electron., vol. 58, no. 6, pp. 2304-2312, Jun. 2011.
- [11] A. Luo, S. Peng, C. Wu, J. Wu, and Z. Shuai, "Power electronic hybrid system for load balancing compensation and frequency-selective harmonics suppression," IEEE Trans. Ind. Electron., vol. 59, no. 2, pp. 723-732, Feb. 2012.
- [12] A. Luo, Z. Shuai, W. Zhu, and Z. John Shen, "Combined system for harmonic suppression and reactive power compensation," IEEE Trans. Ind. Electron., vol. 56, no. 2, pp. 418-428, Feb. 2009.

DADI SRIKANTH currently pursuing his M.Tech in Power Electronics from S.R Engineering College (Autonomous), Warangal, Telangana, India, affiliated to JNTU, Hyderabad. He has done his B.Tech degree from S.V.S Institute of Technology, affiliated to JNTU, Hyderabad, Telangana, India and his fields of interest include Power Systems.

V. SREEPRIYA received her B.Tech degree in Electrical and Electronics Engineering from JNTU, Hyderabad, the M.Tech degree in Power Electronics from JNTU, Hyderabad, Telangana state and she is currently working as Assistant Professor, EEE department at S.R Engineering College (Autonomous), Affiliated to JNTU, Hyderabad, Telangana, India.